

Paganini DAC diagnostic report

□□

ò

G: OS: Starting App... *]#

[* DEBUG: OS: Event Startup1 for BootStrapper *]#

[* DEBUG: OS: Event Startup2 for BootStrapper *]#

[* DEBUG: OS: Event Startup3 for BootStrapper *]#

ConsumerApp Startup3

[* 01/08/07 04:37:50 (SLRunMin,Error): FAILED File Open *]#

[* 01/08/07 04:37:50 (SLRunMin,Error): FAILED File Open *]#

[* 01/08/07 04:37:50 (SLVerboseRun,Info): CFILESYSTEM::FC Duplicate File Found, Deleting... *]#

[* 01/08/07 04:37:50 (SLMinStartup,Info): Writing FRAM FS @ 0x00BE7000 *]#

[* 01/08/07 04:37:50 (SLMinStartup,Info): Writing FRAM FS ID @ 0x00BE7002 *]#

[* 01/08/07 04:37:51 (SLVerboseRun,Info): CFILESYSTEM::Calculating CRC... *]#

[* 01/08/07 04:37:51 (SLVerboseRun,Info): CFILESYSTEM::Done *]#

[* 01/08/07 04:37:51 (SLMinStartup,Info): Writing FRAM FS @ 0x00BE7000 *]#

[* 01/08/07 04:37:51 (SLMinStartup,Info): Writing FRAM FS ID @ 0x00BE7002 *]#

Created PWR_UP histogram

Created error histogram

ESeries FP Driver Started stuccessfully[* DEBUG: OS: Adding E Series FP to scheduler *]#

3264.bmp, width = 496, height=31

done read

done process

DAC Init

External UART Connected

External UART Connected

External UART Connected

[* 01/08/07 04:37:55 (HALRunMin,Info): DSP load 2 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Fitted *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): ICR is 0x20 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): ISR is 0x06 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Opened A:DSP1.BIN *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): File length 0x3210 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): File length valid *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Data length in words 0x0010B0 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Start address 0x000000 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Data 0x0AF080 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Data 0x000102 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Data 0x08F487 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Data 0x000001 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): Data 0x08F487 *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): dsp 2 load complete *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): dsp 2 read semaphore set *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): dsp 2 supports 4 pages *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): dsp 2 read semaphore cleared *]#

[* 01/08/07 04:37:55 (HALRunMin,Info): dsp 2 supports 4 pages *]#

Setting up the I2C:

I2C Write D0 07 21 21 21 02 07 02 07 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 06 07 02 07 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 06 04 02 07 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 06 00 02 07 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 02 00 02 07 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 02 00 02 04 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 02 00 02 00 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 02 00 00 00 01 00 00 00 07 06 07 07 00

I2C Write D0 07 21 21 21 02 00 00 00 01 00 00 00 07 06 07 07 00

I2C Write D0 07 20 21 21 02 00 00 00 01 05 00 00 07 06 07 07 00

I2C Write D0 07 20 21 21 02 00 00 00 01 05 00 00 07 06 07 07 00

```

I2C Write D0 07 20 20 21 02 00 00 00 01 05 05 00 07 06 07 07 00
I2C Write D0 07 20 20 21 02 00 00 00 01 05 05 00 07 06 07 07 00
I2C Write D0 07 20 20 20 02 00 00 00 01 05 05 05 07 06 07 07 00
I2C Write D0 07 20 20 20 02 00 00 00 01 05 05 05 07 06 07 07 00
I2C Write D0 07 20 20 20 02 00 00 00 01 05 05 05 05 06 07 07 00
I2C Write D0 07 20 20 20 02 00 00 00 01 05 05 05 05 06 05 07 00
I2C Write D0 07 20 20 20 02 00 00 00 01 05 05 05 05 06 05 07 00
I2C Write D0 22 20 20 20 02 00 00 00 04 05 05 05 05 06 05 07 00
I2C Write D0 20 20 20 20 02 00 00 00 04 05 05 05 05 06 05 07 00
I2C Write D0 20 20 20 20 02 00 00 00 04 05 05 05 04 06 05 07 00
I2C Write D0 20 20 20 20 02 00 00 00 04 05 05 05 04 06 05 07 00
I2C Write D0 20 20 20 20 02 00 00 00 04 05 05 05 04 04 05 07 00
I2C Write D0 20 20 20 20 02 00 00 00 04 05 05 05 04 04 05 07 00

```

Setting up the DAC:

Resetting...

DAC_Setup_Menu

Processing Icons...OK

```
#[* 01/08/07 04:38:01 (SLVerboseRun,Info): CFILESYSTEM::FC Duplicate File
Found, Deleting... *]#
```

```
#[* 01/08/07 04:38:01 (SLMinStartup,Info): Writing FRAM FS @ 0x00BE7000 *]#
```

```
#[* 01/08/07 04:38:01 (SLMinStartup,Info): Writing FRAM FS ID @ 0x00BE7002
*]#
```

```
#[* 01/08/07 04:38:01 (SLVerboseRun,Info): CFILESYSTEM::Calculating CRC...
*]#
```

```
#[* 01/08/07 04:38:01 (SLVerboseRun,Info): CFILESYSTEM::Done *]#
```

```
#[* 01/08/07 04:38:01 (SLMinStartup,Info): Writing FRAM FS @ 0x00BE7000 *]#
```

```
#[* 01/08/07 04:38:01 (SLMinStartup,Info): Writing FRAM FS ID @ 0x00BE7002
*]#
```

about to Do_DAC

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 0 at <huge>BF:4000 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 1 at <huge>BF:4400 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 2 at <huge>BF:4800 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 3 at <huge>BF:4C00 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 4 at <huge>BF:5000 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 5 at <huge>BF:5400 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 6 at <huge>BF:5800 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP 7 at <huge>BF:5C00 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP release from reset *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA1 read 0x4E *]#
```

DSP2 + DSP3 should be fitted, the others are not fitted.

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 0 not fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA2 read 0x4E *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 1 not fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA3 read 0xA3 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 2 fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): ICR is 0x00 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): ICR is 0x20 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA4 read 0xA4 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 3 fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): ICR is 0x00 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): ICR is 0x20 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA5 read 0x4E *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 4 not fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA6 read 0x4E *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 5 not fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA7 read 0x4E *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 6 not fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP wrote 0xA8 read 0x4E *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): dsp 7 not fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): DSP load 2 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): Fitted *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): ICR is 0x20 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): ISR is 0x06 *]#
```

```
#[* 01/08/07 04:38:01 (HALRunMin,Info): Opened A:DSP1.BIN *]#
```

```

#[* 01/08/07 04:38:01 (HALRunMin,Info): File length 0x3210 *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): File length valid *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): Data length in words 0x0010B0 *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): Start address 0x000000 *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): Data 0x0AF080 *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): Data 0x000102 *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): Data 0x000001 *]#
#[* 01/08/07 04:38:01 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 2 load complete *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 2 read semaphore set *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 2 supports 4 pages *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 2 read semaphore cleared *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 2 supports 4 pages *]#
Successfully loaded DSP441k.bin#[* 01/08/07 04:38:02 (HALRunMin,Info): DSP
load 3 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Fitted *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): ISR is 0x06 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Opened A:DSP_X2.BIN *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): File length 0x1290 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): File length valid *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Data length in words 0x000630 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Start address 0x000000 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Data 0x0AF080 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Data 0x000100 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Data 0x000001 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 3 load complete *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 3 read semaphore set *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 3 supports 4 pages *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 3 read semaphore cleared *]#
#[* 01/08/07 04:38:02 (HALRunMin,Info): dsp 3 supports 4 pages *]#
loaded interpx2
New clk freq : 0
New clk freq : 0
#[* 01/08/07 04:38:02 (HALRunMin,Info): VCXO 1 vcs to 0 *]#
#[* DEBUG: VCXO update [0] 0x06, [1] 0x66, [2] 0x00, [3] 0x55 *]#
I2C Write D2 06 66 00 55
I2C Write D2 16 66 00 55
Checking 1394 interface board
Attempting to setup CONTROL
Resetting...
Writing
Sending...
Sending...
Sending...
Sending...
ALIVE!
Sent all bytes in 4ms
ALIVE! Means the unit has booted-up successfully and the 1394 interface board
is running. The boot-up routine ends here.

Press the front panel buttons to check them, taking care not to leave the
unit in standby mode.
Buttons = 00, enc = 00
Issue! 3.00checksum_in_file
checksumming 0x030000checksum: 0x1E0BA891394 event 0
Buttons = 02, enc = 00
Buttons = 00, enc = 00
Buttons = 08, enc = 00
Current_Audio_Source : Input_Syncs[ Current_Audio_Source ] : Buttons = 00,
enc = 00

```

```
Buttons = 20, enc = 00
Buttons = 00, enc = 00
Buttons = 01, enc = 00
Buttons = 00, enc = 00
Buttons = 01, enc = 00
Buttons = 00, enc = 00
Buttons = 04, enc = 00
Buttons = 00, enc = 00
Buttons = 10, enc = 00
INCREMENT_INPUT called
Buttons = 00, enc = 00
```

Next, a 44.1kS/s source is connected to the selected DAC input

```
New clk freq : 0
New clk freq : 0
#[* 01/08/07 04:39:34 (SLVerboseRun,Info): CFILESYSTEM::FC Duplicate File
Found, Deleting... *]#
#[* 01/08/07 04:39:34 (SLMinStartup,Info): Writing FRAM FS @ 0x00BE7000 *]#
#[* 01/08/07 04:39:34 (SLMinStartup,Info): Writing FRAM FS ID @ 0x00BE7002
*]#
#[* 01/08/07 04:39:34 (SLVerboseRun,Info): CFILESYSTEM::Calculating CRC...
*]#
#[* 01/08/07 04:39:34 (SLVerboseRun,Info): CFILESYSTEM::Done *]#
#[* 01/08/07 04:39:34 (SLMinStartup,Info): Writing FRAM FS @ 0x00BE7000 *]#
#[* 01/08/07 04:39:34 (SLMinStartup,Info): Writing FRAM FS ID @ 0x00BE7002
*]#
New clk freq : 44100
Master->Locking
Different 4 44100(0) 0(0)
Locking->Locked
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 0 at <huge>BF:4000 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 1 at <huge>BF:4400 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 2 at <huge>BF:4800 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 3 at <huge>BF:4C00 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 4 at <huge>BF:5000 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 5 at <huge>BF:5400 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 6 at <huge>BF:5800 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP 7 at <huge>BF:5C00 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP release from reset *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA1 read 0x4E *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 0 not fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA2 read 0x4E *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 1 not fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA3 read 0xA3 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 2 fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ICR is 0x00 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA4 read 0xA4 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 3 fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ICR is 0x00 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA5 read 0x4E *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 4 not fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA6 read 0x4E *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 5 not fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA7 read 0x4E *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 6 not fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP wrote 0xA8 read 0x4E *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 7 not fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP load 2 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ISR is 0x06 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Opened A:DSP1.BIN *]#
```

```

#[* 01/08/07 04:39:58 (HALRunMin,Info): File length 0x3210 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): File length valid *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data length in words 0x0010B0 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Start address 0x000000 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x0AF080 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x000102 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x000001 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 2 load complete *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 2 read semaphore set *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 2 supports 4 pages *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 2 read semaphore cleared *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 2 supports 4 pages *]#
Successfully loaded DSP441k.bin#[* 01/08/07 04:39:58 (HALRunMin,Info): DSP
load 3 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Fitted *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): ISR is 0x06 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Opened A:DSP_X2.BIN *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): File length 0x1290 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): File length valid *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data length in words 0x000630 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Start address 0x000000 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x0AF080 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x000100 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x000001 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 3 load complete *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 3 read semaphore set *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 3 supports 4 pages *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 3 read semaphore cleared *]#
#[* 01/08/07 04:39:58 (HALRunMin,Info): dsp 3 supports 4 pages *]#
loaded interp2

```

The source sample rate is changed to 48kS/s.

```

New clk freq : 0
New clk freq : 48000
Resetting...
#[* DEBUG: VCXO set clocks P0 S0 A0 *]#
#[* DEBUG: VCXO update [0] 0x06, [1] 0x66, [2] 0x00, [3] 0x55 *]#
Resetting...
Resetting...
Resetting...
nning inputs
Different 4 48000(44100) 0(0)
Master->Locking
Locking->Locked
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 0 at <huge>BF:4000 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 1 at <huge>BF:4400 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 2 at <huge>BF:4800 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 3 at <huge>BF:4C00 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 4 at <huge>BF:5000 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 5 at <huge>BF:5400 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 6 at <huge>BF:5800 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP 7 at <huge>BF:5C00 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP release from reset *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA1 read 0x4E *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 0 not fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA2 read 0x4E *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 1 not fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA3 read 0xA3 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 2 fitted *]#

```

```

#[* 01/08/07 04:40:25 (HALRunMin,Info): ICR is 0x00 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA4 read 0xA4 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 3 fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): ICR is 0x00 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA5 read 0x4E *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 4 not fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA6 read 0x4E *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 5 not fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA7 read 0x4E *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 6 not fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP wrote 0xA8 read 0x4E *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 7 not fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP load 2 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): ISR is 0x06 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Opened A:DSP48K.BIN *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): File length 0x2F1C *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): File length valid *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data length in words 0x000FB4 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Start address 0x000000 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x0AF080 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x000102 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x000001 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 2 load complete *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 2 read semaphore set *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 2 supports 4 pages *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 2 read semaphore cleared *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 2 supports 4 pages *]#
Successfully loaded DSP48k.bin#[* 01/08/07 04:40:25 (HALRunMin,Info): DSP
load 3 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Fitted *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): ICR is 0x20 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): ISR is 0x06 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Opened A:DSP_X2.BIN *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): File length 0x1290 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): File length valid *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data length in words 0x000630 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Start address 0x000000 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x0AF080 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x000100 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x000001 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): Data 0x08F487 *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 3 load complete *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 3 read semaphore set *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 3 supports 4 pages *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 3 read semaphore cleared *]#
#[* 01/08/07 04:40:25 (HALRunMin,Info): dsp 3 supports 4 pages *]#
loaded interp2

```